



FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2001,0349	Applic. No. 10/717,337
	Applicant Alexander Benedix, et al.	
	Filing Date November 19, 2003	Group Art Unit 2186

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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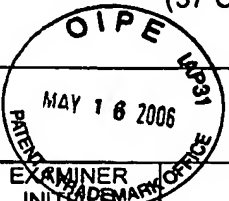
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

SD	O	MoSys Incorporated: "Application Note - Memory Controller Design - Using MoSys High-Speed SGRAM", AN-SG01 Rev. 1.4 - 9/1/1998, Sunnyvale, CA, pp. 1-14.
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EXAMINER /Samuel Dillon/	DATE CONSIDERED 06/01/2006
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
SD	O	Akatsu Motoyasu, et al.: "Parallel Processing (Parallel OLTP, Parallel Batch, Parallel SQL)", NEC Technology Journal, No. 48, 9 th issue, September 25, 1995, pgs. 90-97 and partial English translation thereof					
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